

Name: Diksha Pandey

Designation: Assistant Professor

Department: Electronics and Communication

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Qualifications: Pursuing PhD (Doctor of Philosophy)

Experience: 4.5 Years

Area of Interest: Ad hoc Networks, Sensors, Wireless

Communication, Internet of Things.

Publication: 2 Conferences, 2 Journals

Book/Book Chapter: NA

Patent: NA

Sponsored Projects: NA

Consultancy: NA

CV

Diksha Pandey

Research Scholar | Sensors and Ad hoc Networks

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EXPERIENCE

ASHOKA INSTITUTE OF TECHNOLOGY - VARANASI | ASSISTANT PROFESSOR Feb 2023 – Present | Varanasi, India

→ My responsibilities include delivering lectures of subject Microprocessor and Microcontroller, Electronic Circuit Design, Communication Engineering and Analog Communication.

ACADEMIC PROJECTS

DIGITAL NOTICE BOARD | ARDUINO UNO & C

 $\rightarrow\,$ Digital Notice Board aims to display a notice on LED which reduces the use of paper.

SECURITY BASED NEIGHBOUR SELECTION IN VEHICULAR ADHOC NETWORKS

 $\rightarrow\,$ This Project aims to provide security measures in VANETs to avoid the malicious attackers.

PUBLICATIONS

SECURITY BASED NEIGHBOR SELECTION IN VANETS | INTERNATIONAL JOURNAL OF INNOVATIVE TECHNOLOGY AND EXPLORING ENGINEERING (IJITEE), 2019

→ Scopus Index | Jul 2019

A SURVEY ON PROTOCOL STACKS AT VEHICULAR AD HOC NETWORK | JOURNAL OF EMERGING TECHNOLOGIES AND INNOVATIVE RESEARCH (JETIR), 2018

→ UGC Listed | Dec 2018

A NOVEL APPROACH: LOAD BALANCING WITH FAULT TOLERANT TECHNIQUE TO SOLVE HIDDEN NODE PROBLEM IN MANETS | IEEE World Conference on Applied Intelligence and Computing 2023

ACHIEVEMENTS

- → Workshop on "THz Communication" -SERB IIT BHU, July 2023
- → FDP on Emerging Trend in Multimedia Processing and Security- NIT Patna
- → FDP on Generative AI & Cybersecurity- REVA UNIVERSITY, July 2023
- → Paper presented at Ashoka Institute, Varanasi in IEEE Conference July 2023 on "Load balancing with fault tolerant in MANETs".
- → Certificate of Appreciation by IEEE Graduate talk on topic Machine Learning in Wireless communications on May 2022.
- → Certificate of Participation in the online international workshop on Machine learning in Wireless Communications organized by IIIT Lucknow and University of Essex.
- → Certificate of Participation in the Webinar on VLSI Chip design with FGA: Architecture Design and Hardware Implementation organized by IEEE Student branch MNNIT, Allahabad.

SKILLS

PROFICIENT:

Internet of Things (IoT) • C

EXPERIENCED:

Security in Ad Hoc Networks • LAT_EX

FAMILIAR:

Arduino Uno • Raspberry Pi

EDUCATION

AKTU - LUCKNOW PHD IN ECE

Jan 2023 - Present | UP (India)

SHUATS - PRAYAGRAJ

M. TECH. IN ECE Jul 2017 - May 2019 | School of Engineering CGPA: 6.78 / 10.0

SHUATS - PRAYAGRAJ

B.TECH. IN ECE Jul 2013 - May 2017 | School of Engineering CGPA: 9.09 / 10.0

EXTRACURRICULAR

Departmental Society Coordinator at Ashoka Institute of Technology, Varanasi.